



Reg. No:

G.T.N.ARTS COLLEGE (Autonomous)
(Affiliated to Madurai Kamaraj University)
(Accredited by NAAC with 'B' Grade)

ODD SEMESTER [2019]

INTERNAL ASSESSMENT TEST – II

Programme : **III IT** Date :23 .10.19
Course Code : **17CINF51** Time : 12-1 pm
Course Title : **OPEN SOURCE PROGRAMMING** Max Marks : **25**

Section A

[3 x 2 = 6]

[Answer **ALL** the questions]

1. Write structure of HTML?
2. Write Any 3 String Function in javascript?
3. Write basic concepts of CSS?

Section B

[2 x 5 = 10]

[Answer **ALL** the questions]

4. a) Explain Basic concepts of CSS?[Or]
b) Write short notes on Formatting html documents with example?
5. a) Discuss Basic data types of Java script? [Or]
b) Explain My Sql function in PHP?

Section C

[1 x 9 = 9]

[Answer **ANY ONE** question]

6. Explain HTML table tag with example?
7. Discuss about Looping statement in PHP?

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ODD SEMESTER [2019]

INTERNAL ASSESSMENT TEST – II

Programme : **II IT** Date : 23.10.19
Course Code : **17UITA31** Time : 10.30-11.30 am
Course Title : **DIGITAL PRINCIPLES AND CO** Max Marks : **30**

Section A [6 x 1 = 6]
[Answer ALL questions]

- The Indirect address bit mode is specified using I = _____.
a) 0 b) 2
c) 1 d) 3
- INR control input is used as a _____.
a) INPUT b) INCREMENT
c) INPUT REGISTER d) INPUT LOAD
- DR and AC registers are combined to work on _____ Micro operations.
a) ALU b) Control
c) Memory d) Cache
- A control unit whose binary control variables are stored in memory is called a _____.
a) Micro programmed Control Unit b) Arithmetic Operation Unit
c) Memory unit d) Input control Unit
- The control data register that holds present microinstruction while next address is computed and read from memory is called _____.
a) Data register b) Accumulator
c) Pipeline Register d) Output register
- The clock pulses do not change the state of a register unless the register is enabled by a _____.
a) control signal b) Micro control
c) pulser d) register functional

Section B [2 x 7 = 14]
[Answer ALL the questions]

- a) Describes detailed about Instruction Cycle. [OR]
b) Discuss about Common Bus System.
- a) Discuss about Symbolic Microprogram. [OR]
b) Explain about the different Registers in detail.

Section C [1 x 10 = 10]
[Answer ANY ONE question]

- Explain about Stack Organisation?
- Explain Addressing Modes in Detail?

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ODD SEMESTER [2019]

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ODD SEMESTER [2019-20]

INTERNAL ASSESSMENT TEST - II

Class : II IT Date: 21.10.19
Paper Code : 17UITC31 Time: 12-1 PM
Title of the Paper : OBJECT ORIENTED PROGRAMMING USING C++ Max Marks: 30

Section A

[6 x 1 = 6]

[Answer ALL the questions]

- 1. Constructors should be declared in ___ section.
a)Private b)Public c)Protected d)Main
2. Which Operator can not be used for operator overloading.
a):: b)>> c)&& d)!=
3. ___ function which enables an object to initialize itself, when it is created
a)Virtual b)Constructor c)Friend d)Operator
4. We use ___ function, to automatic type conversion for the user defined datatypes.
a) a)Virtual b)Constructor c)Friend d)Operator
5. Inheritance provides the concept of ___
a) Reusability b)Portability c)Reliability d)Abstraction
6. When the properties of one class are inherited by more than one class is called ___
a)Multilevel b)Multiple c)Hierarchical d)hybrid

Section B

[2 x 7 = 14]

[Answer ALL the questions]

- 7. a) Write short notes on operator function with example?[Or]
b) List out the rules for operator overloading.
8. a) Write a program to implement Multilevel inheritance [Or]
b) What is virtual base class ?Explain with example.

Section C

[1 x 10 = 10]

[Answer ANY ONE question]

- 9. Explain indetail about Constructors in c++ with example.
10. Why do we need virtual function? Write down the rules for virtual function.



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ODD SEMESTER [2019]

INTERNAL ASSESSMENT TEST – II

Programme : **II IT** Date :22 .10.19
Course Code : **17UITC32** Time : 12-1pm
Course Title : **DATA STRUCTURE** Max Marks : **30**

Section A

[6 x 1 = 6]

[Answer ALL questions]

- A _____ is formally defines as a collection of nodes
a) stack b) tree
c) queue d) priority queue
- A binary tree is a finite ordered collection of elements in which one element is designated as root and remaining elements are partitioned into _____disjoint sets
a) three b)four
c)two d) one
- Depth of the tree is calculated using the _____level of any leaf in the tree.
a) Maximum b) Minimum
c) Interior d) lowest
- _____ is the elements in the left subtree are less than the element in the root and the Elements in the right subtree are greater than in root element.
a) Expression tree b) Binary Search Tree
c) Binary tree d) Tree
- _____ is the process of arranging records in order of their keys.
a) Searching b) Sorting
c) hashing d) Deleting
- A data value is placed in its correct position using bubbles during sorting is____
a) heap sort b) Radix sort
c) bubble sort d) Merge sort

Section B

[2 x 7 = 14]

[Answer ALL the questions]

- Describe Bubble sort with an example [OR]
- Explain BST and plot the BST tree for the following example:55,34,76,38,24,67,47,89
- Explain tree traversal in detail with an example. [OR]
- Explain about the insertion sort in detail.

Section C

[1 x 10 = 10]

[Answer ANY ONE question]

- Explain Merge sort with an example?
- Explain types of Binary tree with an example?

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ODD SEMESTER [2019]

INTERNAL ASSESSMENT TEST – II

Programme : **III IT** Date : 21.10.19
Course Code : **17UITC52** Time : 10.30-11.30 am
Course Title : **SOFTWARE ENGINEERING** Max Marks : **30**

Section A [6 x 1 = 6]

[the Answer **ALL** questions]

- The _____ can be used to specify the syntactic structure of symbol strings.
a) Recurrence Relation b) Regular Expressions
c) Modularity d) Algebraic Axioms
- _____ specify actions to be taken when events occur under different sets of conditions.
a) Decision Table b) Event Table
c) Petri nets d) Transition Table
- The _____ reports present information collected from several relationships.
a) Summary b) Database Modification
c) Analysis d) Name List
- _____ is intellectual tool that allows us to deal with concepts apart from particular instances of some concepts.
a) Information hiding b) Encapsulation
c) Abstraction d) Modularity
- _____ coupling involves the use of parameter lists to pass data items between routines.
a) Control b) Stamp
c) Common d) Data
- _____ test cases specify typical operating conditions, typical input values, and expected results.
a) Stress b) Performance
c) Structural d) Functional

Section B [2 x 7 = 14]

[Answer **ALL** the questions]

- a) Describes detailed about PSL/PSA. [OR]
b) Discuss about structured analysis and design techniques.
- a) Discuss any three fundamental design concepts. [OR]
b) Explain integrated top down development.

Section C [1 x 10 = 10]

[Answer **ANY ONE** question]

9. Explain about state oriented notations?
10. Explain modules and modularization criteria?

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ODD SEMESTER [2019]

INTERNAL ASSESSMENT TEST – II

Programme : **III IT** Date : 21 .10.19
Course Code : **17UITC52** Time : 10.30-11.30 am
Course Title : **SOFTWARE ENGINEERING** Max Marks : **30**

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ODD SEMESTER [2019]

INTERNAL ASSESSMENT TEST – II

Programme : **III IT** Date :21 .10.19
 Course Code : **17UITC53** Time : 12-1 pm
 Course Title : **JAVA PROGRAMMING** Max Marks : **30**

Section A

[6 x 1 = 6]

[the Answer **ALL** questions]

- _____ is an abstract class which encapsulates all of the attributes of a visual component.
 - Component
 - applet
 - stream
 - Event
- _____ object has a number of simple drawing functions.
 - Line
 - Graphics
 - fillArc
 - Oval
- _____ is a component that can be used to invoke some action when the user Presses and releases it.
 - Label
 - Button
 - Checkbox
 - List
- _____ returns the input pointer to the previously set mark.
 - close()
 - reset()
 - skip()
 - read()
- _____ tag is used to start an applet from the both an html document and from the JDK appletviewer.
 - <applet>
 - <param>
 - <body>
 - <html>
- A _____ is what is commonly thought of as a window on the desktop.
 - File
 - Exception
 - Frame
 - I/O stream

Section B

[2 x 7 = 14]

[Answer **ALL** the questions]

- Explain check box and check box group with example?[Or]
 - Write short notes on Menu components with example?
- Discuss Order of Applet Intializaton? [Or]
 - Explain File stream with example?

Section C

[1 x 10 = 10]

[Answer **ANY ONE** question]

- Explain about Mouse event with example?
- Discuss about HTML Applet tags with example?

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ODD SEMESTER [2019]

INTERNAL ASSESSMENT TEST – II

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ODD SEMESTER [2019-2020]

INTERNAL ASSESSMENT TEST - II

Class : III IT Date : 23.10.19
Paper Code : 17UITE52 Time : 10.30-11.30pm
Title of the Paper : Cryptography and Network Security Max Marks : 30

Section A

[6 x 1 = 6]

[Answer ALL the questions]

- 1. When there are N systems, keys are needed for Symmetric key cryptography
2. states that if p is prime number an a is positive integer not divisible by p then a^{p-1} = 1 mod p.
3. Which of them are required for Authentication?
4. should be computationally infeasible to find any pair(x,y) such that H(x)=H(y).
5. Security of hash function, the attack can be categories as
6. In MD5, hash code must be bits

Section B

[2 x 7 = 14]

[Answer ALL the questions]

- 7.a) Explain in details about Diffie Hellman key exchange algorithm [Or]
8. a) Explain the basic theorems available in number theory [OR]
b) Explain the RSA algorithm with example.

Section C

[1 x 10 = 10]

[Answer ANY ONE question]

- 9. Discuss indetail about Block cipher mode of operation.
10. What is Message Authentication Codes? Explain with MD5 algorithm.



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ODD SEMESTER [2019-2020]

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ODD SEMESTER [2019-20]

INTERNAL ASSESSMENT TEST – I

Program : **I B.Sc(IT) A&B**
Course Code : **17UBAN11**
Course Title : **Basics of Retail Marketing**

Date :
Time :
Max Marks :**30**

Section A

[6 x 1 = 6]

[Answer **ALL** the questions]

1. The Word "Retail" is derived from _____ language.
(a) French (b) Italian
(c) German (d) Latin
2. Retailers provide _____.
(a) Place of Utility (b) Time Utility
(c) Possession Utility (d) All of the above
3. Retailers are _____.
(a) Advicers (b) Assemblers
(c) Middleman (d) None of the above
4. The word Market is derived from _____.
(a) Latin (b) Greek
(c) German (d) American
5. Which of the following is not included in the list of macro environment variables _____?
(a) Prevailing Economic conditions and Political Manifestors
(b) Changes in legislation and emerging new technologies
(c) Financial conditions and culture
(d) None of the above
6. The Store format which is more spacious _____.
(a) Super Market (b) Compact Super Market
(c) Metro Store (d) Express Store

Section B

[2 x 7 = 14]

[Answer **ALL** the questions].

7. (a) Write about the difference between Retailers and Wholesaler? [OR]
(b) What are the functions of Retailing?
8. (a) Explain the Classification of Retailers by Philip Kotler. [OR]
(b) Write shortly 1. Demographic environment 2. Technological environment.

Section C

[1 x 10 = 10]

[Answer **ANY ONE** question]

9. Explain briefly the Types of Retailers.
10. What is Retail Environment? Explain the Classification of Retail Environment.

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ODD SEMESTER [2019-20]

INTERNAL ASSESSMENT TEST – I

Program : **I B.Sc(IT) A&B**
Course Code : **17UBAN11**
Course Title : **Basics of Retail Marketing**

Date :
Time :
Max Marks :**30**

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EVEN SEMESTER [2018-19]

I NTERNAL ASSESSMENT TEST – I

Class : **II IT A&B** Date :20.08.2019
Paper Code :17UITA31 Time : 10.30-11.30am
Title of the Paper : **Digital Principles and CO** Max Marks : **30**

Section A

[6 x 1 = 6]

[Answer **ALL** the questions]

- 1.A gate whose output is H if any input is H is _____
a.OR b. AND c.NAND d.NOT
2. A gate whose output is H only when all inputs are H is _____
a.OR b. AND c.NAND d.NOT
- 3.On a K-Map, an Octet contains how many 1's _____
a.8 b. 4 c.6 d.2
- 4.Same input conditions never occur in normal operations which is called_____
a. Comparator b. Multiplexer c. Don't Care Condition
d. Demultiplexer
- 5.A 16-to-1 Multiplexer having _____ control Inputs
a.8 b. 4 c.6 d.2
6. Demultiplexer means one input into _____ outputs
a. only one b.two c.many d.four

Section B

Answer the following: (7 x2 =14)

7. a) Explain about the Universal Logic Gates with truth table and logic Circuits

(OR)

- b) Explain the Don't Care Condition with an Example.

8. a) Explain the Sum of Products method with an Example

(OR)

- b) Explain the Encoder in detail an Example.

Section C

Answer any one: (10x1=10)

9. a)Convert Hexadecimal to Decimal : i)9AF ii)E8D6
b)Convert Octal to Hexadecimal : i) 2576 ii)231
10. Explain in detail the 16-to-1 Multiplexer with a pin diagram.

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EVEN SEMESTER [2018-19]

I NTERNAL ASSESSMENT TEST – I

Class : **II IT A&B** Date :20.08.2019
Paper Code : 17UITA31 Time : 10.30-11.30am
Title of the Paper : **Digital Principles and CO** Max Marks : **30**

Section A

[6 x 1 = 6]

[Answer **ALL** the questions]

- 1.A gate whose output is H if any input is H is _____
a.OR b. AND c.NAND d.NOT
2. A gate whose output is H only when all inputs are H is _____
a.OR b. AND c.NAND d.NOT
- 3.On a K-Map, an Octet contains how many 1's _____
a.8 b. 4 c.6 d.2
- 4.Same input conditions never occur in normal operations which is called_____
a. Comparator b. Multiplexer c. Don't Care Condition
d. Demultiplexer
- 5.A 16-to-1 Multiplexer having _____ control Inputs
a.8 b. 4 c.6 d.2
6. Demultiplexer means one input into _____ outputs
a. only one b.two c.many d.four

Section B

Answer the following: (7 x2 =14)

7. a) Explain about the Universal Logic Gates with truth table and logic Circuits

(OR)

- b) Explain the Don't Care Condition with an Example.

8. a) Explain the Sum of Products method with an Example

(OR)

- b) Explain the Encoder in detail an Example.

Section C

Answer any one: (10x1=10)

9. a)Convert Hexadecimal to Decimal : i)9AF ii)E8D6
b)Convert Octal to Hexadecimal : i) 2576 ii)231
10. Explain in detail the 16-to-1 Multiplexer with a pin diagram.



Reg. No:

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ODD SEMESTER [2019-20]

INTERNAL ASSESSMENT TEST – I

Class : **II IT** Date: 21.08.19
 Paper Code : **17UITC31** Time: 12-1 PM
 Title of the Paper : **OBJECT ORIENTED PROGRAMMING USING C++** Max Marks: **30**

Section A

[6 x 1 = 6]

[Answer **ALL** the questions]

- A _____ variable provides an alternative name for a previously defined variable.
 - Reference Variable
 - Static Variable
 - Class Variable
 - Structure Variable
- _____ gives the compiler the details about the functions
 - Function Call
 - Function Definition
 - Function Prototype
 - Function Overload
- _____ is expanded in line when it is invoked.
 - Inline
 - Friend
 - Virtual
 - Static
- By default, the members of a class are _____.
 - Private
 - Public
 - Protected
 - Const
- A data member of a class can be declared as a _____ and is normally used to maintain values common to the entire class.
 - Dynamic
 - Const
 - Friend
 - Static
- _____ function has to access to the private data of these classes.
 - Recursive Function
 - Friend Function
 - Static function
 - Inline Function

Section B

[2 x 7 = 14]

[Answer **ALL** the questions]

- Write short notes on reference variable with example?[Or]
 - Explain in detail about Control statements in c++?
- Describe about Inline Function with example program. [Or]
 - What is friend function? What are the merits and demerits of using friend function.

Section C

[1 x 10 = 10]

[Answer **ANY ONE** question]

- What is object oriented Programming ? Explain them.
- How do you specify the class and its object with example program?

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ODD SEMESTER [2019-20]

INTERNAL ASSESSMENT TEST – I

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Section A

[6 x 1 = 6]

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Section B

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ODD SEMESTER [2019-20]
INTERNAL ASSESSMENT TEST – I

Class : **III IT** Date: 16.08.19
Paper Code : **17UITC51** Time: 12-1 PM
Title of the Paper : **DATA COMMUNICATION AND COMPUTER NETWORKS** Max Marks: **30**

Section A

[6 x 1 = 6]

[Answer **ALL** the questions]

- _____ is used to send files from one system to another under user command
 - FTP
 - SMTP
 - TELNET
 - HTTP
- _____ is equivalent to picoseconds.
 - 1s
 - 10⁻³s
 - 10⁻⁹s
 - 10⁻¹²s
- Optical fiber uses _____ to transmit information.
 - Voice
 - light
 - Analog
 - digital
- The Expansion of CRC is _____
 - Cyclic Redundancy Check
 - Cyclic Repeat check
 - Cyclic Read check
 - Cyclic Rotate check
- Serial transmission means that a group of bits is transmitted _____
 - Group
 - parallel
 - Block
 - one by one
- The error detection method used by the higher layer protocol is called ____
 - Checksum
 - LRC
 - VRC
 - parity bit

Section B

[2 x 7 = 14]

[Answer **ALL** the questions]

- Discuss OSI model? [Or]
 - Describe Transmission media?
- Discuss Longitudinal Redundancy Check(LRC) ? [Or]
 - Describe the function of Network Interface Card?

Section C

[1 x 10 = 10]

[Answer **ANY ONE** question]

- Explain about Error Correction Method?
- Discuss about Modulation and Demodulation.

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ODD SEMESTER [2019-20]
INTERNAL ASSESSMENT TEST – I

Class : **III IT** Date: 16.08.19
Paper Code : **17UITC51** Time: 12-1 PM
Title of the Paper : **DATA COMMUNICATION AND COMPUTER NETWORKS** Max Marks: **30**

Section A

[6 x 1 = 6]

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ODD SEMESTER [2019]

INTERNAL ASSESSMENT TEST – I

Programme : **III IT** Date : 17.08.19
 Course Code : **17UITC52** Time : 10.30-11.30 am
 Course Title : **SOFTWARE ENGINEERING** Max Marks : **30**

Section A [6 x 1 = 6]

[the Answer **ALL** questions]

4. The ability of a program to perform a required function under stated conditions for a stated period of time.
 - a) Scalability
 - b) Reliability
 - c) Clarity
 - d) Correctness
5. A _____ process goal is system should be delivered within 12 months.
 - a) Qualitative
 - b) Product
 - c) Clearness
 - d) Quantitative
3. The _____ task develops user's manuals, installation instructions, principles of operation and other supporting documents.
 - a) Service
 - b) Planning
 - c) Assurance
 - d) Publication
4. The _____ cost estimation first estimates the cost to develop each module subsystem.
 - a) Top Down
 - b) Bottom Up
 - c) Left to Right
 - d) Right to Left
5. COCOMO Stands for _____.
 - a) Constructive Cost Model
 - b) Constructive Continue Model
 - c) Cost Constructive Model
 - d) Continue Constructive Model
6. $ACT = (DSI_{Added} + \text{_____}) / DSI_{Total}$.
 - a) $DSI_{Modified}$
 - b) DSI_{Source}
 - c) $DSI_{Enhance}$
 - d) DSI_{Adapt}

Section B [2 x 7 = 14]

[Answer **ALL** the questions]

- 7.a) Describes detailed about phased life cycle model. [OR]
- b) Discuss various project size categories.
- 8.a) List out steps of Delphi cost estimation technique. [OR]
- b) Explain estimating software maintenance costs.

Section C [1 x 10 = 10]

[Answer **ANY ONE** question]

9. Discuss any five quality and productivity factors?
10. Explain various software cost factors?

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ODD SEMESTER [2019]

INTERNAL ASSESSMENT TEST – I

Programme : **III IT** Date : 17.08.19
 Course Code : **17UITC52** Time : 10.30-11.30 am
 Course Title : **SOFTWARE ENGINEERING** Max Marks : **30**

Section A [6 x 1 = 6]

[the Answer **ALL** questions]

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ODD SEMESTER [2019-20]
INTERNAL ASSESSMENT TEST – I

Class : **III IT** Date: 17.08.19
Paper Code : **17UITC53** Time: 12-1 PM
Title of the Paper : **JAVA PROGRAMMING** Max Marks: **30**

Section A

[6 x 1 = 6]

[Answer ALL the questions]

- _____ is the collection of objects of similar type
 - Class
 - Object
 - Data type
 - Inheritance
- Methods that have the same name but different parameters list and different definition is called _____
 - Method Overriding
 - Method Overloading
 - Inheritance
 - Data hiding
- The Mechanism of deriving of a new class from an old one is called _____
 - Class
 - Data abstraction
 - Inheritance
 - Static members
- The Visibility modifiers are also known as _____
 - Control Modifiers
 - Integer type
 - Access Modifiers
 - float type
- _____ class defines a number of methods that allow us to a variety of string manipulation tasks
 - String
 - Exception
 - Class
 - Interface
- An _____ is a condition that is caused by a runtime error in the program
 - Array
 - Exception
 - Package
 - Inheritance

Section B

[2 x 7 = 14]

[Answer ALL the questions]

- a) Explain Basic concepts of object oriented programming?[Or]
b) Write short notes on constructors with example?
- a) Discuss Inheritance concepts with example? [Or]
b) Explain packages with example?

Section C

[1 x 10 = 10]

[Answer ANY ONE question]

- Explain about Exception handling with example?
- Discuss about String handling function with example?

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ODD SEMESTER [2019-20]

INTERNAL ASSESSMENT TEST – I

Class : **III IT** Date: 17.08.19
Paper Code : **17UITC53** Time: 12-1 PM
Title of the Paper : **JAVA PROGRAMMING** Max Marks: **30**

Section A

[6 x 1 = 6]

[Answer ALL the questions]

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Section B

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Section C

[1 x 10 = 10]

[Answer ANY ONE question]

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- Discuss about String handling function with example?



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ODD SEMESTER [2019-2020]
INTERNAL ASSESSMENT TEST – I

Class : **III IT** Date : 20.8.19
Paper Code : **17UITCA3** Time :10.30-1.30pm
Title of the Paper : **Cryptography and Network Security** Max Marks : **30**

Section A [6 x 1 = 6]
[Answer **ALL** the questions]

- _____ is an action that compromises the security of information owned by an organization.
a)Security Attack b)Security Mechanism
c)Security Service d)Security Analysis
- Which is the example of Passive Attack?
a)Masquerade b)Replay
c)Denial of Service d)Traffic Analysis
- Restoring the Plain Text from the Ciphertext is _____
a)Enciphering b)Deciphering
c)Encryption d) cryptography
- The key length in IBM's original LUCIFER algorithm was _____ bits
a)128 b)64 c)196 d)56
- Which is not present in AES algorithm?
a) s-box b) shift row c)mix columns d)Feistel structure
- Number of rounds available in DES is _____
a)10 b)16 c)14 d) 11

Section B [2 x 7 = 14]
[Answer **ALL** the questions]

- a) Explain in details about Security Attack. [Or]
b)List out the security services available in network.
- a) Explain any two Substitution techniques with example. [OR]
b)Explain any two Transposition Techniques with example.

Section C [1 x 10 = 10]
[Answer **ANY ONE** question]

- Explain in detail about DES algorithm.
- Discuss about AES algorithm.



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ODD SEMESTER [2019-2020]
INTERNAL ASSESSMENT TEST – I

Class : **III IT** Date : 20.8.19
Paper Code : **17UITCA3** Time :10.30-1.30pm
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Section A [6 x 1 = 6]
[Answer **ALL** the questions]

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a)Security Attack b)Security Mechanism
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- Which is the example of Passive Attack?
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Section C [1 x 10 = 10]
[Answer **ANY ONE** question]

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